

REMARKS

Reconsideration and withdrawal of all grounds of rejection, and allowance of the pending claims are respectfully requested in light of the amendments and remarks made herein. Claims 1-5, 7-13 remain pending. Claims 6 and 14-15 have been cancelled without prejudice.

The specification stands objected to as failing to provide an antecedent basis for the claimed subject matter “computer readable medium” in claim 13. In response the specification has been amended to provide an antecedent basis, no new matter has been added. Accordingly, applicants request removal of the objection.

Claims 1-15 stand objected to due to informalities. In response the claims have been amended to obviate the informalities. Further, applicants note with regard to claims 1 and 10 and the Office Action’s question as to where the processes reside, page 3, lines 10-15 of the specification states: “As an example, if e.g. two different operating systems are running on a single processor at a given time, where one of the operating systems is a real-time operating system running the high priority thread and the other operating system is a non-real time system running the low priority thread, then the priority of the low priority thread cannot be raised high enough to reach the priority of the high priority thread.” Accordingly, applicants request removal of the objection.

Claims 1-2, 4-11 and 13-15 stand rejected under 35 USC 103(a) as being unpatentable over Kush (USP No. 6,874,144) in view of Jones et al. (“Hard Real time with RTX on windows NT”) . Claims 3 and 12 stand rejected under 35 USC 103(a) as being unpatentable over Kush in view of Jones and in further view of Werres et al. (USP No. 5,295,264).

In response applicants have amended independent claim 1 to recite the limitations of “A method *for prevention of priority inversion without priority promotion mechanism when* executing processes with different priorities in a multiprocessing environment comprising execution of a low priority process and a high priority process *and optionally at least one intermediate priority process having a priority between that of the low priority process and the high priority process*, where the high priority process and the low priority process share a given resource, the method comprising the step of: temporarily raising an effective priority of the low priority process when the low priority process is going to use the shared resource, *wherein the step of raising the effective priority comprises executing/assigning an additional process accessing the shared resource on behalf of the low priority process where the additional process has a priority higher than the at least one intermediate priority process in the multiprocessing environment.*” Applicant can find nothing in Kush or Jones, alone or in combination, which teaches the above limitations. Independent claims 10 and 13 recite similar limitations.


Since Kush and Jones do not teach all of the limitations of independent claims 1, 10 and 13, it cannot render the present invention unpatentable. For at least the above cited reasons, Applicant submits that Claims 1, 10 and 13 are patentable over Kush and Jones.

With regard to claims 2-5, 7-9 and 11-12 these claims depend from the independent claim discussed above, which have been shown to be allowable in view of the cited reference. Accordingly, each of claims 2-5, 7-9 and 11-12 are also allowable by virtue of its dependence from an allowable base claim.

For all the foregoing reasons, it is respectfully submitted that all the present claims are patentable in view of the cited references. Entry of the Amendment and a Notice of Allowance is respectfully requested.

Respectfully submitted,

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A handwritten signature in black ink, appearing to read 'Thomas J. Onka', written over a horizontal line.

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